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The SiO₂ is etched and an arsenic implant forms the N+ regions. SILICON VLSI TECHNOLOGY Fundamentals, Practice and Modeling By Plummer, Deal and Griffin 30 © 2000 by Prentice Hall Upper Saddle River, NJ. fSolutions Manual N+ N+ P P+ P+ N N+ P The N+ regions are driven-in to their final junction depth.

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Solutions Manual SILICON VLSI TECHNOLOGY 8 © 2000 by Prentice Hall Fundamentals, Practice and Modeling Upper Saddle River, NJ. By Plummer, Deal and Griffin 2.3. The cross-section below illustrates a simple bipolar transistor fabricated as part of a silicon IC. (See also Figure 1-32.)

S G D S G D N P N N P N P WELL P+

Text: Silicon VLSI Technology: Plummer, Deal and Griffin Prerequisite: EE/MSE486 (Fundamentals of Integrated Circuit Technology) or EE/MSE 502 (Introduction to MEMS) or EE 527 (Solid State Laboratory Techniques) or equivalent exposure to basics of photolithography and diffusion/reaction problems, or consent of instructor.

EE/MSE 528 Physics and Modeling of VLSI Fabrication

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